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Practical Parallel Processing

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• Memory Usage (*space*)

VRIJE Parallel Systems SCIEN VINCERE Parallel Systems **1. Shared-Memory Architecture** lab M CPU fast communication dedicated machines M CPU CPU CPU M 2. Message-Passing Architecture Collection of - Processors

- Memory
- Interconnection Network



- slower communication
- simple, cheap

general-purpose PC's



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How? Communication Layer

- *Pvm* (Parallel Virtual Machine) or *MPI* (Message Passing Interface)
 - transparant
 - platform-independent
- Functions
 - create processes on other machines
 - send & receive messages





Aspects of Practical Parallelization

- 1. System-dependency
- 2. Inherent Parallelism
- 3. Software Engineering
- 4. Performance Analysis

1. System-dependency



• Heterogeneous Systems

VRUE

SCIENTIF

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- different processing powers
- different communication speeds
- combinations of shared memory & message passing architectures

2. Inherent Parallelism

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- SCIENTIA

• Trivial parallelizable

- replicated trials (multiple experiments)
 - => script
- multiple jobs
 - => job management

2. Inherent Parallelism II



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Difficult to Parallelize

- Simulations
 - Synchronization protocol
 - Model dependent
- Virtual 3D world
 - Tessalation, lighting calculations, rendering...
- > Performance depends on various aspects, like data structures
- > Optimizations are possible, but strongly depend on problem/algorithm

Example: Parallel Simulation

BE WINERSITE/7

SSE





3. Software Engineering



- Understandable, Maintainable
 - tangled code!
- Flexible
 - separate parallel code
 - Eg.: reuse sequential algorithm, so it can be adapted
- Reusable
 - trade-off generic program <> performance

4. Performance Analysis

- Detection of performance bottlenecks
 - For example

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Parallel

vstems

- communication-computation ratio
- load imbalances
- Scalability analysis
 - bigger problem => more computers
- Calculation of optimal number of processors



Parallel

Performance Analysis Tools







Theoretical Conclusions



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Sequential world

- Separation hardware program (3GL)
 With abstract model for architecture: Von Neuman
- Java: platform-independence
- .net: language-independence

Parallel world

Ultimate goal: match software - hardware

No universal abstract model for parallel architectures!

Conflict generality <> efficiency

Performance is program- and hardware dependent

Efficient programs should be developed specifically ...

